

MPEG Data Packet



Fig. 1

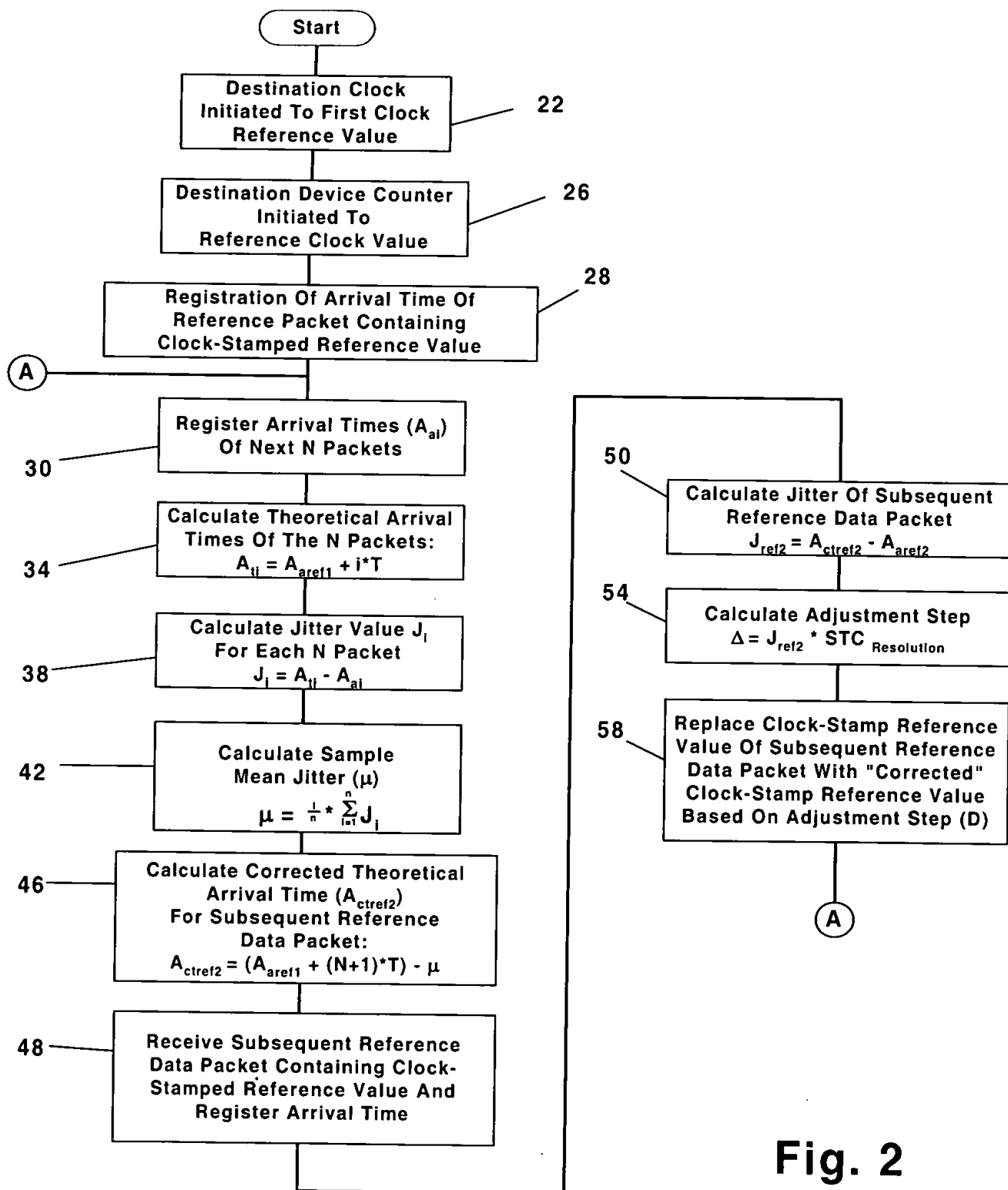


Fig. 2

The diagram illustrates the timing of a TDMA system. The top horizontal line represents the Source Device, and the bottom horizontal line represents the Destination Device. The time axis is marked with various events and intervals.

Source Device Timeline:

- D_{tr} : Transmission time from the start of the first frame to the start of the first frame at the destination.
- T : Time interval between the start of the first frame and the start of the second frame.
- D_{t1} , D_{t2} , D_{t3} : Time intervals between the start of the first, second, and third frames, respectively, and the start of the first frame at the destination.

Destination Device Timeline:

- A_{tr} : Arrival time of the first frame at the destination.
- A_{t1} , A_{a1} , A_{a2} , A_{t2} , A_{a3} , A_{t3} : Arrival times of the first, second, and third frames, respectively, at the destination.
- J_1 , J_2 , J_3 : Processing times for the first, second, and third frames, respectively.

Reference Time:

- D_{ref} : Reference time interval from the start of the first frame to the start of the first frame at the destination.

Fig. 3